SYCLOPS

Deliverable 6.5 – Communication, Networking and Dissemination Plan and Activities M36

GRANT AGREEMENT NUMBER: 101092877

Project acronym:	**SYCLOPS**
Project full title:	**Scaling extreme analYtics with Cross architecture acceLeration based on OPen Standards**
Call identifier:	**HORIZON-CL4-2022-DATA-01-05**
Type of action:	**RIA**
Start date:	**01/01/2023**
End date:	**31/12/2025**
Grant agreement no:	**101092877**

D6.5 – Communication, Networking and Dissemination Plan and Activities M36

Executive Summary: This deliverable is the final progress update of the Communication, Networking and Dissemination Plan and Activities at M36, with all progress measured against the plan submitted in the D6.1 deliverable.

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Participating Partners:	All Partners		
Version:	1.0	**Status:**	Draft
Deliverable Type:	R - Document	**Dissemination Level:**	PU - Public
Official Submission Date:	31.12.2025	**Actual Submission Date:**	31.12.2025

Disclaimer

The SYCLOPS consortium consists of the following partners:

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1	EURECOM	EUR	FR
2	INESC ID - INSTITUTO DE ENGENHARIADE SISTEMAS E COMPUTADORES, INVESTIGACAO E DESENVOLVIMENTO EM LISBOA	INESC	PT
3	RUPRECHT-KARLS-UNIVERSITAET HEIDELBERG	UHEI	DE
4	ORGANISATION EUROPEENNE POUR LA RECHERCHE NUCLEAIRE	CERN	CH
5	HIRO MICRODATACENTERS B.V.	HIRO	NL
6	ACCELOM	ACC	FR
7	CODASIP S R O	CSIP	CZ
8	CODEPLAY SOFTWARE LIMITED	CPLAY	UK

Document Revision History

Version	Description	Contributions
0.1	Skeleton and outline	**EUR**
0.2	First draft	**ACC**
0.3	Second draft with input from all partners	**ALL**
1.0	Final submission-ready draft	**EUR**

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Statement of Originality

This deliverable contains original unpublished work except where clearly indicated otherwise. Acknowledgement of previously published material and of the work of others has been made through appropriate citation, quotation or both.

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Executive Summary

This document Deliverable D6.5 is the final report on the Communication, Networking, and Dissemination Plan and Activities for the SYCLOPS project. It covers the full 36-month duration of the project, from January 2023 through December 2025, with a particular focus on the final reporting period (M24-M36). This deliverable serves as the definitive record of how the SYCLOPS consortium executed its strategic mandate to foster an open, innovative European ecosystem for accelerated AI and analytics. The report measures the project's cumulative progress against the plans originally submitted in D6.1 and subsequently refined in the D6.3 and D6.4 updates. In doing so, the report confirms that the consortium has not only met but significantly exceeded the Key Performance Indicators (KPIs) established at the project's inception.

1 Introduction

This is the M36 update of the Communication, Networking and Dissemination Plan and Activities deliverable. This is part of WP6, and relates specifically to Objective 5 of the SYCLOPS Project which is outlined below,

Obj 5 (Exploitation, Dissemination, Standardization): To foster an open, innovative European ecosystem for accelerated AI and analytics by leading and feeding back to standardization efforts and communicating project outcomes via already well-established dissemination channels and developer communities.

The Communication, Networking and Dissemination Plan was outlined in D6.1, with a list of KPIs and Objectives outlined for both Dissemination and Communication. An updated plan was was submitted at M12 in deliverable D6.3, and at M24 in deliverable D6.4. This deliverable is the final update at M36 that will cover the progress made since submission of deliverable D6.4 in the third and final year of the SYCLOPS project with reference to the identified KPIs and Objectives.

The rest of this document is organized as follows. Section 2 of this report will examine the Dissemination KPIs and Strategy, while Section 3 will focus on the Communication KPIs and Objectives.

2 Dissemination

Dissemination aims to share scientific results, contribute to the wider academic ecosystem, advance technologies and maximise the impact of the project on society. In D6.1 deliverable, we outlined the following objectives of dissemination in SYCLOPS.

- D.O.1: Ensure maximum visibility of the project in the target audiences via appropriate key messages.
- D.O.2: Diffuse the scientific and technological knowledge generated in the project within and beyond the project's consortium in a timely manner.
- D.O.3: Establish liaisons with other projects and initiatives for knowledge and innovation transfer.
- D.O.4: Engage the targeted audiences to get feedback and validate results.
- D.O.5: Attract potential users/clients and stimulate the appropriate market segments to support the project's exploitation strategy.
- D.O.6: Encourage the development of further outcomes in new initiatives.

In D6.1 deliverable, we also outlined the list of dissemination mechanisms that are planned to achieve the aforementioned objectives as listed below.

- D.M.1: Organization of project events
- D.M.2: Conference/workshop participation
- D.M.3: Scientific publications
- D.M.4: Community Building, Stakeholder Engagement
- D.M.5: Synergies with projects
- D.M.6: Internal dissemination
- D.M.7: Standardization contributions

2.1 Dissemination Strategy

In the D6.1 deliverable, we also outlined a Dissemination Plan and Strategy. The dissemination activities of SYCLOPS will be divided into four phases:

1. Raise Awareness (M1 – M12): This phase includes the creation of awareness about the project by using various channels such as social media platforms, project website, flyer, banner, posters.
2. Inform and Interact (M12 – M24): This phase includes the provision of detailed information about the project to the identified - stakeholders through webinars, workshops, or other interactive sessions.
3. Promote (M24 – M36): This phase includes the promotion of SYCLOPS benefits to potential users through targeted campaigns such as email campaigns or publications on websites, as well as engaging with stakeholders (e.g. workshops, high-level roundtables) to spread awareness about the project's objectives, benefits and outcomes.
4. Post-Project Communication: This phase includes communications and updates on the progress made during and after the project implementation such as evaluation reports or success stories related to SYCLOPS's contribution towards AI acceleration market on both an Industry level and Policy level.

We are currently in M36 of this plan, and hence at the completion of the “Promote” phase. During this phase, with all activities of the project having completed successfully, all dissemination mechanisms D.M.1 to D.M.7 were active at a high intensity with the goals of (i) maximizing visibility of project launch across target audiences (D.O 1), (ii) diffusing knowledge generated in the project (D.O.2), (iii) establishing liaison with other relevant projects (D.O 3), and (iv) start engaging target audience to receive early feedback (D.O.4). We have taken several steps towards meeting both these objectives. Here, we provide an update with respect to each dissemination mechanism.

2.1.1 D.M.1 Project Events

Organizing dedicated project events allowed the consortium to control the narrative and engage with specific target groups. Over the course of the project, the nature of these events evolved from general introductory workshops to highly technical, hands-on sessions. The consortium organized the following specific events to engage target technical communities.

- **Workshops:**
  - oneAPI Iberian Tour (Dec 2023): Organized by INESC-ID in Lisbon, focusing on performance profiling and the Cache-Aware Roofline Model.
  - Adaptyst Workshop (Sep 2025): Organized by CERN (hybrid format). The workshop focused on the Adaptyst profiling tool, targeting computer scientists and physicists in the High Energy Physics (HEP) community.
- **Hackathons:**
  - CERN Computing Hackathon (Oct 2023): Focused on SYCL-ROOT and Cling integration for CMS scientists.
  - RISC-V Summit Europe Hackathon (June 2024): Led by Codasip, challenging students to accelerate neural networks on RISC-V cores.
  - IWOCL Hackfest (April 2024): Heidelberg University served as a mentor for the SYCL hackfest.
- **Tutorials:**
  - The project delivered 5 tutorials on SYCL and AdaptiveCpp at major conferences: ISC 23, IWOCL 23, IWOCL 24, ISC 24, and IWOCL 25. The "AdaptiveCpp Best Practices" tutorial at IWOCL 25 provided training on the compiler infrastructure developed in the project.

2.1.2 D.M.2 Conference/workshop attendance

Participation in conferences allowed the project to reach established communities in HPC, embedded systems, and data analytics. The consortium strategically targeted venues that aligned with the dual pillars of the project: software standards (SYCL/OpenCL) and hardware standards (RISC-V).

2.1.2.1 The International Workshop on OpenCL & SYCL (IWOCL)

The project's engagement with IWOCL has been sustained and deepening over the three years. The 2025 edition, held in Heidelberg, Germany, from April 7-11, was a landmark event for SYCLOPS. With UHEI serving as the local chair, the project had excellent visibility. The consortium presented multiple contributions, including a tutorial on "AdaptiveCpp Best Practices," which educated the community on the advanced features of the compiler developed under SYCLOPS. Additionally, the project presented the paper "Adaptivity in AdaptiveCpp: Optimizing Performance by Leveraging Runtime Information During JIT-Compilation," which showcased the culmination of the project's research into runtime adaptivity. This venue also

hosted the Khronos SYCL Working Group meetings, where project partners delivered the "State of the Union" address for the SYCL Safety Critical (SC) group, demonstrating the project's influence on the standard itself. The interaction at IWOCL was critical for validating the project's software stack against the needs of the wider heterogeneous computing community.

2.1.2.2 RISC-V Summits (Europe and North America)

In parallel, the project maintained a heavy presence at major RISC-V events to promote its hardware-focused results. At the RISC-V Summit Europe 2025 in Paris (May 2025), CSIP played a central role. The company showcased its latest developments in RISC-V processor customization and the implementation of CHERI (Capability Hardware Enhanced RISC Instructions) for memory safety. Later in the year, the consortium extended its reach to the RISC-V Summit North America 2025 in Santa Clara (October 2025). This global stage was utilized to present the mature outcomes of the project to a wider audience of silicon vendors and system architects. Presentations such as "Streamlining Verification for Custom Compute" by CSIP personnel underscored the project's contribution to solving practical challenges in the adoption of open hardware. The sustained presence at these summits confirms the project's status as a key player in the European and global RISC-V ecosystem.

2.1.2.3 Embedded World 2025

To address the industrial and embedded market segments, CSIP also participated in Embedded World 2025 in Nuremberg (March 2025). This massive trade fair provided an opportunity to demonstrate the commercial viability of the technologies developed. Talks on enabling RISC-V processor customization without re-verification addressed a specific industrial pain point, directly supporting the objective of attracting potential users and stimulating market segments (D.O.5).

2.1.2.4 Scientific and Academic Conferences

Beyond these industry-heavy events, the academic partners ensured rigorous scientific dissemination. CERN presented the "Adaptyst" profiling tool at CHEP 2024 (Computing in High Energy Physics) in Kraków and HPEC 2025 (High Performance Extreme Computing), targeting the niche but influential community of scientific computing experts. Adaptyst also won the best short paper award at HPEC 2025. INESC-ID was highly active in the parallel processing community, presenting papers at EuroPar 2024 and HeteroPar 2025, as well as the IEEE International Symposium on Workload Characterization (IISWC) in 2024 and 2025.

2.1.3 D.M.3 Publications

Over the 36 months, the consortium has published a total of 21 research articles in various high-impact journals and conferences on a range of topics from low-level hardware design to high-level application benchmarks. A detailed list of publication is available on the SYCLOPS website.

2.1.4 D.M.4 Community

Community building was integral to the project's strategy, moving beyond one-way dissemination to foster a thriving ecosystem around the project's technologies. The consortium targeted five specific communities: SYCL/oneAPI, RISC-V, Hardware/Edge, and Bioinformatics.

2.1.4.1 SYCL and oneAPI Community

The project's engagement with the SYCL community has been deeply structural. CPLAY's pivotal role in the Unified Acceleration Foundation (UXL) has allowed SYCLOPS to directly influence the future of open accelerated computing. By contributing the oneAPI Construction Kit to the foundation, the project ensured that its work on enabling SYCL for RISC-V would be maintained by a broad industry consortium. Furthermore, UHEI's consistent presence at IWOCL and ISC with AdaptiveCpp tutorials has built a user base for this compiler, positioning it as a leading open-source implementation of SYCL. The engagement extended to webinars, such as the one presented by INESC-ID on achieving 9x speedup in epistasis detection using oneAPI, which demonstrated the practical power of the ecosystem to developers.

2.1.4.2 RISC-V Community

Engagement with the RISC-V community was driven by CSIP's leadership. Beyond standard presentations, the project focused on nurturing the developer ecosystem through hackathons. The hackathon organized at the RISC-V Summit Europe 2024, which challenged students to accelerate neural networks on RISC-V cores using Codasip Studio, is a prime example of this "deep" engagement. Additionally, the project's advocacy for the CHERI security extensions within the RISC-V International working groups has helped galvanize a community focused on hardware-enforced memory safety.

2.1.4.3 Hardware and Edge Computing Community

HIRO Microdatacenters led the engagement with the hardware and edge infrastructure community. Through discussions with PICMG (PCI Industrial Computer Manufacturers Group), HIRO advocated for the inclusion of CXL-capable SoCs (including RISC-V) in the COM-HPC standard. This engagement is crucial for the long-term industrial uptake of the project's hardware concepts, ensuring they align with the form factors used in real-world edge deployments.

2.1.4.4 Bioinformatics Community

On the application side, ACCELOM facilitated engagement with the bioinformatics sector. ACCELOM hosted webinars demonstrating the SYCL genomics acceleration library to its Indian partners. These events translated the project's abstract technical gains into tangible benefits for bioinformaticians and clinicians, widening the project's impact beyond the core computer science demographic.

2.1.5 D.M.5 Project Synergy

SYCLOPS project was a part of te DataNexus Cluster. Comprising seven Horizon Europe projects—Graph-Massivizer, EXTRACT, NEARDATA, EXA4MIND, EMERALDS, SYCLOPS, and EFRA, this cluster represents a unified European front on "Extreme Data."

Throughout the final year, the cluster's activities intensified. The projects collaborated on a joint brochure and maintained a shared presence at key events like the European Big Data Value Forum (EBDVF). The most significant output of this synergy is the DataNexus White Paper, which the cluster is working on and slated for publishing in 2026. This document is expected to synthesize the collective findings of the seven projects, identifying gaps in current technology and proposing a research agenda for future extreme data analytics. Additionally, the synergy with the VITAMIN-V project remained strong. Given the complementary nature of

the projects (VITAMIN-V focusing on cloud services for RISC-V, SYCLOPS on hardware acceleration), SYCLOPS project participated in workshops, such as the "Open Source RISC-V Software Workshop" at the RISC-V Summit.

2.1.6 D.M.6 Internal Dissemination

Internal dissemination was vital for maintaining the coherence of such a technically diverse consortium. The "Research Highlights" series, embedded in the monthly plenary meetings, proved to be an effective mechanism for internal knowledge transfer. These deep-dive sessions allowed partners to explain their specific advancements, such as the details of the RISC-V Vector extension implementation or the intricacies of the SYCLDB merge join algorithm, to the rest of the consortium. This facilitated the integration of components, such as the use of CERN's AdaptivePerf tool by other partners for benchmarking their own software stacks. The General Assemblies in Sep 2023, Sep 2024, March 2025, Dec 2025 provided the necessary face-to-face time to align on the final integration steps and the preparation of the M36 demonstrators.

2.1.7 D.M.6 Standardization Contributions

The project's objective to "lead and feed back to standardization efforts" was pursued actively, with tangible results in both the software and hardware domains. In deliverable D6.4, we outlined the contributions from SYCLOPS to standardization efforts on three fronts: (i) edge hardware design, (ii) SYCL and oneAPI, and (iii) RISC-V. In the final year of the project, we have continued to contribute to standardization efforts on all three fronts.

Edge hardware standards. HIRO is working towards creating an edge datacenter hardware architecture standard based on an industrial form factor (Com HPC) that is well supported by European OEM's and widely used by European Embedded and Industrial PC Industry. Of particular focus has been the use of PCIe 6.0 switching and CXL memory.

SYCL standardization. UHEI and CPLAY continued their active participation in the Khronos Group SYCL Working Group. Insights gained from developing AdaptiveCpp led to specific proposals for the SYCL specification, particularly regarding the handling of runtime information during JIT compilation. This feedback loop ensures that the standard evolves to support real-world implementation challenges.

RISC-V standardization. Standardization is a key pillar of CSIP's strategy to ensure interoperability and long-term sustainability of RISC-V solutions. As a founding member of RISC-V International, CSIP actively influences specifications and contributes to open standards that shape next-generation computing. While SYCLOPS focused on RISC-V Vector Extension (RVV)-enabled cores and SYCL-based software stacks, CSIP's broader roadmap includes CHERI (Capability Hardware Enhanced RISC Instructions) - a security extension widely seen as the next major evolution in RISC-V. Future plans involve enabling CHERI on the RISC-V core delivered under SYCLOPS and making the Vector Processing Unit (VPU) platform CHERI-ready. In parallel, CSIP aligns with RVA23, the emerging architecture profile mandated by major OS vendors, reinforcing readiness for Linux and Android ecosystems.

CSIP has actively shaped the RISC-V ecosystem through technical contributions, collaborations, and participation in global standardization bodies:

- **Specification Development and Upstream Contributions**

- Delivered extensive updates to the CHERI-RISC-V specification, including over 240 merged pull requests and ongoing refinements to security extensions.
- Contributed 400+ LLVM patches upstream to enhance vector support, reinforcing compatibility with RVV and future security features.

- **Open-Source Enablement**
  - Maintained and extended CHERI Linux, QEMU, and FreeRTOS ports for CHERI-RISC-V platforms.
  - Supported integration of security and vector capabilities in toolchains, ensuring readiness for next-generation RISC-V profiles.
- **Collaborations with Academia and Industry**
  - Partnered with leading institutions such as University of Cambridge (CHERI spec, QEMU, LLVM), University of Manchester (MMU-less RISC-V Linux), and University of Birmingham (Zephyr on CHERI-RISC-V).
  - Worked with Google on CHERI Linux and specification alignment.
- **Working Groups and Ecosystem Engagement**
  - Participation in RISC-V International conferences and technical committees, reinforcing Codasip's influence as a founding member.
  - Active roles in CHERI Alliance working groups (Linux, FreeRTOS, QEMU, certification programs).

2.2 Dissemination KPIs

In the D6.1 deliverable, the following KPIs were identified for dissemination. A new column (KPI Results at M36) has been added to the table. Clearly, SYCLOPS has met and exceeded all dissemination KPIs.

Table 1: Dissemination KPIs

Activity	KPI Targets (Lifetime)	Final Results at M36
D1 - Project Events	2 workshops, 3 demo events	2 Workshops, 3 Hackathons, 5 Tutorials
D2 – Conferences	20 events attended	32 Distinct Events Attended (with ~40 presentations)
D3 – Publications	10 papers	21 Peer-Reviewed Publications
D4 – Community	5 communities, 2 webinars	5 Communities (SYCL, RISC-V, HW, Edge, Bio), 2 Webinars

Activity	KPI Targets (Lifetime)	Final Results at M36
D5 - Project Synergy	3 projects	7 Projects (DataNexus Cluster + VITAMIN-V)
D6 - Internal	10 internal events	4 General Assemblies, Monthly Plenary Meetings

2.3 Future Dissemination Planning

While the funded period of SYCLOPS concludes at M36, the dissemination of its results will transition to a sustainable post-project phase. All tools and libraries developed in SYCLOPS will continue to be maintained by their respective owners on GitHub as research and development work is expected to continue well beyond the project. The contributions to Khronos and RISC-V International standardization bodies are permanent. The working groups will continue to refine the specifications based on the proposals submitted by the project. Two out of three use case partners (CERN, ACC) have already integrated the solutions developed in SYCLOPS into their official pipelines. These partners will continue to use SYCLOPS tools in the future and acknowledge the contributions of SYCLOPS in their dissemination efforts as and when relevant.

3 Communication

Through effective communication, we aim to engage with wider audiences to generate interest in the project. This should attract potential users, developers and contributors while also generating demand for the SYCLOPS Project by effectively conveying the problem statement, and successes of the SYCLOPS Project in solving this problem.

In D6.1, we identified the following communication objectives:

- C.O.1: Create project awareness among potential adopters/users in the general public
- C.O.2: Convey the project's concept, goals, results through key messages in communication material
- C.O.3: Activate a community of users, collect feedback
- C.O.4: Prepare for the exploitation of the project's results
- C.O.5: Targeted dissemination of the project's results
- C.O.6: Foster adoption of the project's results in industry and society

In D6.1, we also outlined a list of communication mechanisms that we will use to achieve the aforementioned objectives.

- C.M.1: Project website
- C.M.2: Social media presence
- C.M.3: Project blog
- C.M.4: Traditional media
- C.M.5: Communication material

3.1 Communication Strategy

Similar to dissemination, communication in SYCLOPS also follows the four-phased approach: (i) Raise Awareness (M1-M12), (ii) Inform and interact (M12-M24), (iii) Promote (M24-M36), and (iv) post-project communication. At M36, we are at the end of promote phase of communication. In the final year of the project, we have used all communication mechanisms to target objectives C.O.1 through C.O.6. Below, we described our activities for each communication mechanism.

3.1.1 C.M.1: Project Website

At M12, we created the SYCLOPS website (https://www.syclops.org) and ensured a responsive, intuitive design that allows visitors to quickly find the latest updates of the project. In the final year, the website was updated to serve as a repository for the project's legacy. This included uploading the final public deliverables, linking to the scientific publications, and hosting several blogs about work carried out in SYCLOPS. In the second year, CPLAY implemented Fathom Analytics to obtain privacy-compliant insights into user behaviour. Website analytics showed a steady increase in traffic driven by the release of technical blog posts and major event announcements. However, since first quarter of 2025, CPLAY had internal issues that resulted in a loss of analytics and statistics for over 6 months. After CPLAY officially exited the project, ACC took over the dissemination efforts and reenabled Google Analytics based tracking.

3.1.2 C.M.2: Social Media Presence

The project employed a "hub and spoke" social media strategy. While the SYCLOPS accounts on X (Twitter), LinkedIn, and YouTube served as the central hubs, partner organizations also played a key role in communicating work done in SYCLOPS via their own channels. Large partners like CERN, CPLAY, and CSIP regularly reshared project content, exposing it to their already-existing follower bases. We had setup an aggressive KPI target for social media presence as shown in Section 3.2 of 500 followers. At M36, we have accrued 180 followers on LinkedIn, 67 followers on X, and 10 subscribres on youtube, leading to a total of 257 followers. While this number does not reach the KPI target, we would like to emphasize two aspects: (i) these numbers do not reflect the large number of followers of some invididual partners like CERN, CPLAY, and CSIP, who received SYCLOPS updates directly from partner social media accounts, and (ii) we have far exceeded the number of interactions that we targeted with a much smaller, yet active and interested, base of followers. Thus, we do not believe this short fall to be a major problem.

3.1.3 C.M.3: Project Blog

The project blog was instrumental in translating complex technical achievements into accessible narratives. The "Technical Deep Dive" series was particularly successful and has produced several high-quality blog posts that are linked to deep dive videos and associated Git repositories. For each such blog, partners wrote detailed posts explaining the mechanics of their contributions. A highlight was the blog post announcing the RISC-V support in the oneAPI Construction Kit, which was widely shared within the RISC-V community. These posts often served as the "landing page" for traffic generated from social media, guiding users into the deeper technical content of the project. In total, we have posted 27 blogs covering all aspects of the SYCLOPS project, from low-level hardware to application use cases. We are actively working on publishing the last few blogs advertising various outcomes of SYCLOPS from the final year and we expect these to be online by Jan 2026.

3.1.4 C.M.4: Traditional Media

Traditional media engagement was marked by dissemination of the following seven major press releases.

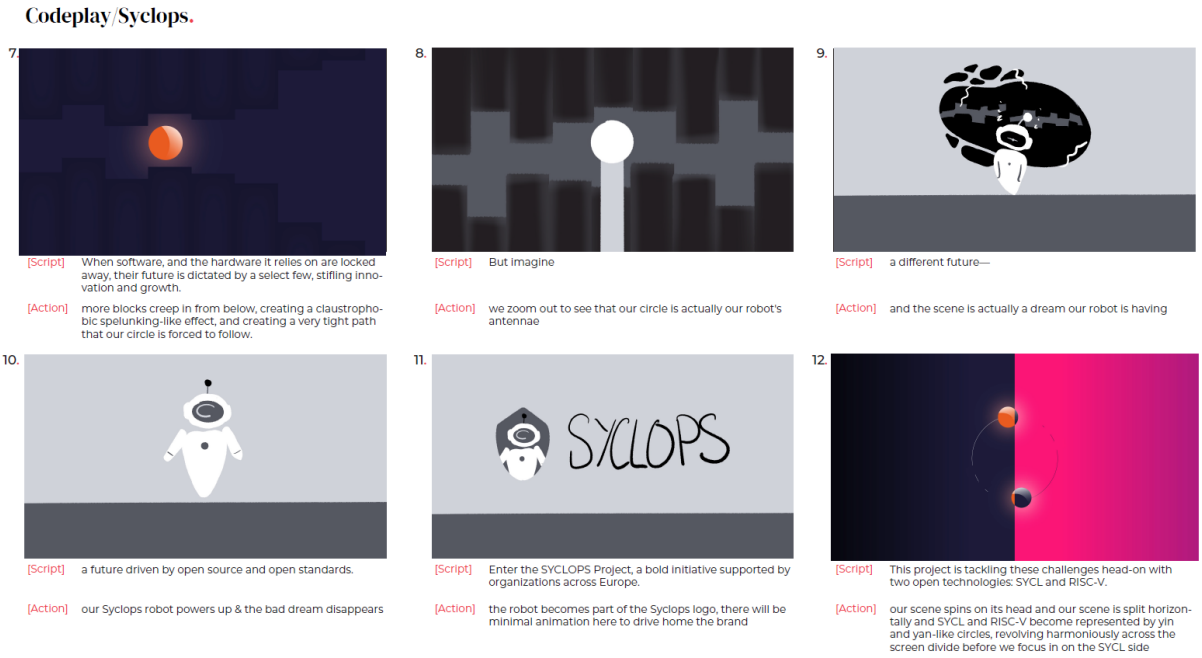
1. **Vision Statement**: A press release titled "How the SYCLOPS project democratizes AI acceleration" was published in August 2023.
2. **Hardware Innovation**: CSIP introduced the 700 RISC-V processor family in October 2023 to support custom compute applications.
3. **Cluster Formation**: The project joined the DataNexus Cluster to coordinate extreme data mining efforts across seven EU-funded projects in July 2024.
4. **Software Milestone**: Codeplay announced native RISC-V support in the oneAPI Construction Kit in September 2024.
5. **Standardization**: The oneAPI Construction Kit joined the UXL Foundation in November 2024 to foster open governance.
6. **EC event**: The press release from DataNexus about SYCLOPS' participation in the Future-Ready event organized by ADRA in Feb 2025
7. **Final Technical Update**: The project concluded its major outreach with "A New Era for Edge AI: Codasip's Custom Vector Processor Drives the SYCLOPS Mission" in November 2025.

Additionally, partner-driven press activities, such as CSIP's announcements regarding CHERI technology, ASIL-D certification, and **EnSilica** partnerships, indirectly promoted the SYCLOPS

research agenda by demonstrating the commercial traction of the underlying technologies related to RISC-V and custom compute.

3.1.5 C.M.5: Communication Material

The SYCLOPS logo and brand has been established across our social channels and website. A major update in the second phase of the project was the development of a user-friendly promotional video to communicate the goals of SYCLOPS to a broader and more general audience. We worked together with Posh Gecko to make the promotional video for SYCLOPS. Figure below shows an example story board generated by Posh Gecko based on which the SYCLOPS video was made. The final resulting video has been published on youtube and is has also been linked to appear on the landing page of the SYCLOPS website.



The consortium has also produced several factsheets and brochures. This included an initial SYCLOPS brochure and a dedicated factsheet, followed by a joint DataNexus cluster brochure and flyer for extreme data challenges. These activities culminated in the preparation of a joint White Paper for Extreme Data Analytics, which is consolidating research findings for policy and industry stakeholders, and expected to be published in 2026.

Finally, we have also worked on drafting a user-friendly short podcast summarizing the goals and achievements of SYCLOPS project as a whole using GenAI. This podcast will be publicized on all social media channels in Jan 2026 together with a press release about the completion and achievements of SYCLOPS.

3.2 Communication KPIs

In D6.1, the following KPIs were identified to measure the success of each communication activity over the lifetime of the project. A new column (KPI Results at M36) has been added to the table.

Table 2: Communication KPIs

Activity	KPI Targets	KPI Progress at M36
C1 – Website	5000 unique visitors, 1 min. average visit duration, 10000 page views	1367 visitors, 9 mins 58 secs avg. engagement time, 17k page views
C2 – Social Media	500 accumulative followers, 250 interactions	257 followers, 250+ interactions
C3 – Blog	50 posts, 100 interactions	27 blogs, 100+ interactions
C4 – Media	3 press releases	7 press releases, 1 audio podcast
C5 – Materials	5 project factsheets/brochures and banners, 9 eNewsletters, 2 videos, 5 blog posts in EC mechanism	2 factsheets, 2 brochures, 10 videos

As can be seen in the table, we substantially exceeded our KPI targets for some activities, while lagging behind for others. For instance, the C1 KPI target was 5000 unique visitors while we record 1367 at M36. The reason for this shortfall was explained earlier in this document. But we would like to point out here that even with fewer users, we have surprassed our KPI for page views with over 17k views and a long retention period of 9 minutes indicating the high quality nature of our blog posts on the websites. Similarly, while we lag the C2 and C3 KPI target in terms of cumulative accumulative followers and counts of posts, we have far exceeded the number of interactions in both cases. Finally, while we have published fewer factsheets and brochures than anticipated, this has in no way impeded the adoption of software developed in SYCLOPS. For instance, DPCPP and ACPP are both industry standard compilers today that have had a huge impact on the SYCL community and beyond. RVV accelerator developed by CSIP is SYCLOPS is a production-grade product offering from CSIP.

4 Conclusion

This deliverable, D6.5, marks the successful conclusion of the communication and dissemination activities for the SYCLOPS project. Over the past 36 months, the consortium has executed a robust and adaptive strategy that has effectively successfully bridged the gap between advanced research and practical adoption.

The transition through the "Raise Awareness," "Inform and Interact," and "Promote" phases was executed with precision. In the final year, the project capitalized on its mature technical results to make a significant impact at premier global forums like IWOCL, the RISC-V Summits, and Embedded World. The scientific legitimacy of the project was cemented through high-impact publications in various peer-reviewed venues.

Crucially, the project has left a sustainable legacy. The open-source tools developed in SYCLOPS, like AdaptiveCpp and oneAPI Construction Kit, are now well established in the developer community. The contributions to standards have permanently influenced the trajectory of open hardware and software. The strategic alliances formed within the DataNexus cluster have strengthened the European data ecosystem. As Europe continues to strive for digital sovereignty and technological leadership in AI and Big Data, the foundational work disseminated by SYCLOPS will serve as a critical building block for the next generation of extreme analytics solutions. The consortium partners, enriched by this collaboration, are well-positioned to carry these innovations forward into future commercial and research endeavors.